

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently Amended) An inter-processor communication apparatus of a mobile communication system comprising:
  - a data-FIFO configured to store a receiving data;
  - a slave-logic configured to control a writing operation of the receiving data for the data-FIFO and count the length of the receiving data until an end-tag signal is inputted;
  - a length-FIFO directly connected to the slave-logic and configured to store the data length counted by the slave-logic; and
  - a CPU configured to continuously read the data stored in the data-FIFO as much as the data read from the length-FIFO when an interrupt signal is inputted from the slave-logic.
2. (Original) The apparatus of claim 1, wherein the slave-logic counts the length of the receiving data until an end tag signal is inputted.
3. (Original) The apparatus of claim 2, wherein the read data length is one frame data length.

4. (Original) The apparatus of claim 1, wherein the slave-logic stores the counted data length in the length-FIFO when the end tag signal is inputted and outputs an interrupt signal to the CPU.

5. (Original) The apparatus of claim 1, wherein the CPU continuously reads the data stored in the data-FIFO by 1 byte unit as much as the data length stored in the length-FIFO.

6. (Currently Amended) An inter-processor communication method of a mobile communication system, comprising the steps of:

storing a receiving data in a first region;

counting the length of the receiving data stored in the first region;

checking whether an end tag is received;

storing the counted data length in a second region when the end tag is received and outputting an interrupt signal to a CPU; and

continuously reading the data stored in the first region by the CPU as much as the data length stored in the second region, wherein a slave-logic is connected to the first region and the second region and configured to control said storing the receiving data and said counting the length of the receiving operation until the end-tag is received.

7. (Original) The method of claim 6, wherein the first and the second regions are FIFO.
8. (Original) The method of claim 6, wherein the data length stored in the second region is one frame of data length.
9. (Original) The method of claim 6, wherein the CPU continuously reads the data by 1 byte unit.
10. (Currently Amended) An inter-processor communication apparatus of a mobile communication system, comprising:
- means for storing received data in a first region;
  - means for counting ~~[[the]]~~ a length of the received data stored in the first region~~[[;]]~~ ~~means for~~ and for checking whether an end tag of the received data is received, wherein the means for counting counts until the end tag is received;
  - means for storing the counted data length in a second region when the end tag is received and outputting an interrupt signal to a CPU; and
  - means for reading the data stored in the first region by the CPU as much as the data length stored in the second region.

11. (New) The apparatus of claim 10, wherein the means for counting is directly connected to the first region and the second region.

12. (New) The apparatus of claim 1, wherein the slave-logic is directly connected to the data-FIFO.

13. (New) The apparatus of claim 1, wherein the CPU is connected to the data-FIFO and the length-FIFO.

14. (New) The apparatus of claim 1, wherein the counted data length of data is read in a single operation from the first region.

15. (New) The method of claim 6, wherein the continuously reading comprises performing a signal read operation to read the counted data length of data from the first region.